Claims

1. A data write-in method for flash memory, wherein the flash memory comprises at least two flash chips, and the method includes:

a. partitioning the physical blocks in the two flash chips to odd logical block addresses and even logical block addresses, respectively;

b. receiving a data write-in instruction and analyzing the beginning logical address corresponding to the writing operation from the data write-in instruction;

c. obtaining according to the beginning logical address the logical block address needed to be written, deciding the parity of the logical block address needed to be written, and selecting the corresponding flash chip between the two flash chips according to the parity of the logical block address needed to be written;

d. detecting whether the other flash chip needs to be programmed or erased after the programming or erase instruction is directed to the physical block corresponding to the logical block address in the corresponding flash chip;

2. The data write-in method for flash memory according to claim 1, wherein it further comprises the following step:

e. if the other flash chip needs to be programmed or erased, directing programming or erase instruction to the other flash chip.

3. The data write-in method for flash memory according to claim 1, wherein it further comprises the following step:

f. if the other flash chip do not need to be programmed or erased, then judge whether the operation performed to the corresponding physical block in step d is finished.

4. The data write-in method for flash memory according to claim 3, wherein it further comprises: if the operation performed on the corresponding physical block has been finished, judge whether the data write-in instruction has been finished; if the operation performed to the corresponding physical block has not been finished, return to step d.

5. The data write-in method for flash memory according to claim 3, wherein that: if the data write-in instruction has been finished, return to step b; if the data write-in instruction has not been finished, return to step c.

6. The data write-in method for flash memory according to claim 4, wherein that: the step b further comprises obtaining the number of sectors needed to be written from the data writing operation instruction.

7. The data write-in method for flash memory according to claim 6, wherein that: the method further comprises judging whether the data writing operation instruction has been finished by subtracting the number of written sectors from the number of need-to-be-written sectors.